

REMARKS

The Examiner's Official Action dated January 10, 1995 has been received and its contents carefully noted. Filed concurrently herewith is a *Request for a Three Month Extension of Time* which extends the shortened statutory period for response to July 10, 1995. Accordingly, Applicant respectfully submits that this response is being timely filed.

Summary of the Invention

Claims 1-5 and 21-23 are currently pending in the present application and claims 1, 5 and 21-23 are independent. Claims 6-8 have been withdrawn from consideration as being directed to a non-elected species. The claims of the application are generally directed to an electro-optical device as shown in Figures 1(A) and 1(B) and described at pages 8-9 of the present application. Specifically, the present invention is directed to an electro-optical device wherein a pixel electrode is superimposed on both the gate line that drives the pixel electrode and either an exclusive wiring or an adjacent gate line. As shown in Figure 1(A), capacitance C1 is formed by the pixel electrode and the gate line X_n and capacitance C2 is formed by the pixel electrode and the wiring X_n' . In this manner, the ΔV is reduced and the quality of the display is increased.

Prior Art Rejections

The Official Action rejects claims 1 and 21 as anticipated by U.S. Patent 4,582,395 to Morozumi. The Official Action asserts that Figures 6 and 7 of Morozumi disclose a pixel electrode 41 provided on a substrate 43, and superposed on gate line 36 with an insulator 47 therebetween and superposed on a ground wiring (exclusive wiring) 37 with an insulator therebetween to form a charge holding capacitor as recited in column 6, lines 37-39.

Claims 1 and 21 have been amended herewith to further recite that a capacitance between the claimed pixel electrode and gate line and a capacitance between the claimed pixel electrode and wiring are the same as each other. It is respectfully submitted that Morozumi fails to disclose this further limitation and therefore cannot anticipate claims 1 and 21 as amended. In view thereof, reconsideration of the outstanding rejection is respectfully requested.

The Official Action next rejects claim 5 as anticipated by U.S. Patent 4,773,737 to Yokono et al. Referring to Figure 3A, the Official Action asserts that pixel electrode (G_{14}) of n-th row (G_m) and m-th column (S_n) is provided on an opposite side of the data line (S_n) to the pixel electrode (G) of the $(n+1)$ -th row (G_{m+1}) and m-th column (S_n).

Claim 5 has been amended herewith to recite that the claimed pixel electrodes of n-th row and $(n+1)$ -th row are superposed on the data line and the gate line of n-th row and $(n+1)$ -th row respectively. It is respectfully submitted that this limitation as now recited in claim 5 is not disclosed or suggested by the prior art of record, and clearly not by Yokono et al., and reconsideration is therefore requested.

The Official Action next rejects claims 1-3 and 22 as anticipated by U.S. Patent 5,193,017 to Iwai et al. As noted above, claim 1 has been amended to further recite that a capacitance between the claimed pixel electrode and gate line and a capacitance between the claimed pixel electrode and wiring are the same as each other. It is asserted that this feature is not disclosed by Iwai et al. either and that claims 1-3 are therefore allowable over the prior art of record.

With respect to claim 22, this claim has been amended herewith to include the limitations of dependent claim 4. Specifically, claim 22 has been amended to recite that the difference between the area shared by the first gate line and the pixel electrode and the area shared by the second gate line and the pixel electrode is not more than the sum of such areas. It is respectfully submitted that this feature is not disclosed or suggested by any of the prior art of record and therefore, in view of the indicated allowability of claim

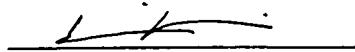
4, it is respectfully asserted that claim 22 is also allowable and reconsideration of the rejection of claim 22 is requested.

The Official Action next rejects claim 23 under 35 U.S.C. §102(b) as being anticipated by JP No. 60-54478. The Examiner refers to Figure 3 and asserts that electrode 25₂ overlaps gate line 22₁ and ground line 22₂, each covered by an anodic oxide coating 27. In response, it is noted that reference numeral 25₂ refers to a capacitor electrode and not to a pixel electrode as asserted in the Official Action. Since this capacitor electrode 25₂ is not a pixel electrode, it is respectfully submitted that this reference fails to disclose each and every limitation as presently recited in claim 23, particularly the claimed pixel electrode and associated structure. Therefore, claim 23 is not anticipated by this reference and reconsideration is accordingly requested.

Conclusion

For all of the above reasons, it is respectively asserted that claims 1-8 and 21-23 are now also in proper condition for allowance and reconsideration of the pending rejections is respectively requested. If the Examiner believes that any further discussions would be beneficial in this case, he is invited to contact the undersigned.

Respectfully submitted,


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